## PATENT

## IN THE UNITED STATES PATENT OFFICE

cants:

Reno L. Sanchez et al.

ianee:

Xilinx, Inc.

Title:

Method and System for Inserting Probe Points in

FPGA-Based System-On-Chip

Serial No.:

10/082,517

File Date:

02-22-02

Examiner:

Magid Y. Dimyan

Art Unit:

2825

Docket No.:

X-998 US

Conf. No.:

5981

COMMISSIONER FOR PATENTS

P.O. Box 1450

Alexandria, Virginia 22313-1450

## SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Dear Sir:

Pursuant to 37 C.F.R. 1.56, Applicants bring to the attention of the Examiner the three (3) references listed in the attached Substitute for Form 1449A/PTO. A copy of each is enclosed herein.

The enclosed reference(s) is/are being cited after the receipt of a first office action but prior to any final action.

Citation of the above documents shall not be construed as an admission that the documents are necessarily prior art with respect to the instant invention. Citation of the above documents shall not be construed as a representation that a search has been made other than as described above. Also, the citation of the above documents shall not be construed as an admission that the information cited herein is, or is considered to be, material to patentability as defined in §1.56(b).

Respectfully submitted,

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180.00 DA

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Reg. No. 37,652

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450, on January 6, 2004.

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PTO/SB/08A (10-01)
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Substitute for form 1449A/PTO

## INFORMATION DISCLOSURE STATEMENT BY APPLICANT

(use as many sheets as necessary)

Sheet 1 of 1

Complete if Known				
10/082,517 /	5981			
February 22, 2002				
Reno L. Sanchez				
2825				
Magid Y. Dimyan				
	10/082,517 / February 22, 2002 Reno L. Sanchez 2825			

			U.S. PATENT	DOCUMENTS	
Examiner Initials *	Cite No.1	Document Number  Number - Kind Code (if know	Publication Date  MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear
		US- 10/082,440	02-22-02	Sanchez et al.	
		US- 10/082,441 /	02-22-02	Hwang et al.	
		US- 10/082,518 /	02-22-02	Hwang et al.	
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	FOREIGN PATENT DOCUMENTS							
	Cite No.¹	Foreign Patent Document	Publication Date	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear	T 6		
		Country Code <sup>3</sup> -Number <sup>4</sup> -Kind Code <sup>5</sup> (if known)	MM-DD-YYYY	Applicant of offer becomen				
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Examiner Signature	Date Considered	
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<sup>\*</sup>EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

Applicant's unique citation designation number (optional). See Kinds of USPTO Patent Documents at www.uspto.gov or MPEP 901.04. The Inter Office that issued the document, by the two-letter code (WIPO Standard ST.3). For Japanese patent documents, the indication of the year of the reign of the Emperor must precede the serial number of the patent document. Kind of document by the appropriate symbols as indicated on the document under WIPO Standard ST. 16 if possible. Applicant is to place a check mark here if English language Translation is attached.

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